



JST30E-1200BW 30A TRIAC

Rev.A.1.1

DESCRIPTION:

The JST30E-1200BW triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. JST30E-1200BW snubberless triac is especially recommended for use on inductive loads. Package TO-263 is RoHS compliant.

MAIN FEATURES

ABSOLUTE MAXIMUM RATINGS

| Parameter | Symbol | Value | Unit |
|--|--------------|---------|------|
| Storage junction temperature range | T_{stg} | -40-150 | |
| Operating junction temperature range | T_j | -40-125 | |
| Repetitive peak off-state voltage ($T_j=25^\circ\text{C}$) | V_{DRM} | 1200 | V |
| Repetitive peak reverse voltage ($T_j=25^\circ\text{C}$) | V_{RRM} | 1200 | V |
| RMS on-state current ($T_c 096^\circ\text{C}$) | $I_{T(RMS)}$ | 30 | A |

Non repetitive surge peak on-state current
(full cycle , t_p)

| | | | |
|--|----------|-----|----|
| Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.8) | V_{pp} | 2.5 | kV |
|--|----------|-----|----|

ELECTRICAL CHARACTERISTICS (unless otherwise specified)

| Symbol | Test Condition | Quadrant | Value | | Unit |
|----------------------|--|----------|-------|------|------|
| I_{GT} | $V_D=12V$ $R_L=33$ | - - | MAX. | 50 | mA |
| V_{GT} | | - - | MAX. | 1.3 | V |
| V_{GD} | $V_D=V_{DRM}$ $T_j=125$ $R_L=3.3k$ | - - | MIN. | 0.15 | V |
| I_L | $I_G=1.2I_{GT}$ | - | MAX. | 90 | mA |
| | | | | 100 | |
| I_H | $I_T=500mA$ | | MAX. | 80 | mA |
| dV/dt | $V_D=800V$ Gate Open $T_j=125$ | | MIN. | 1200 | V/s |
| (dI/dt) _c | $V_D=800V$ $T_j=125$ | | MIN. | 25 | A/ms |
| t_{on} | $I_G=40mA$ $I_A=200mA$ $I_R=20mA$ T | | | | |

ORDERING INFORMATION

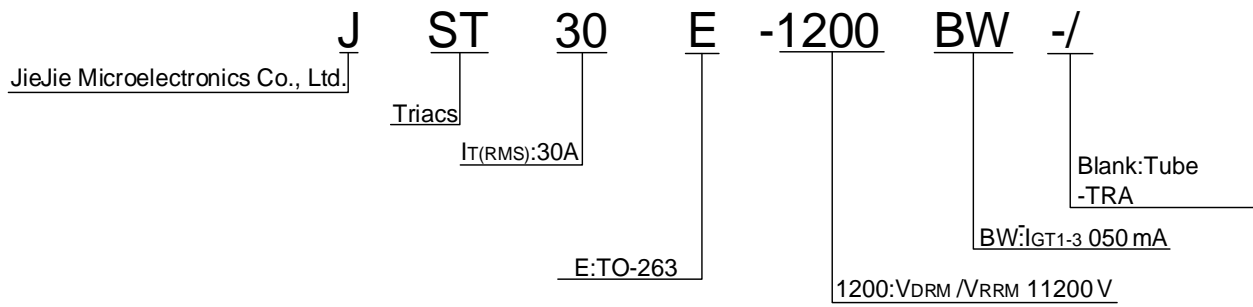


FIG.1: Maximum power dissipation versus RMS on-state current

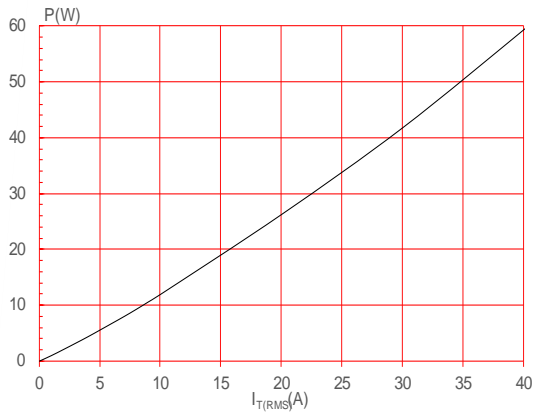


FIG.2: RMS on-state current versus case temperature

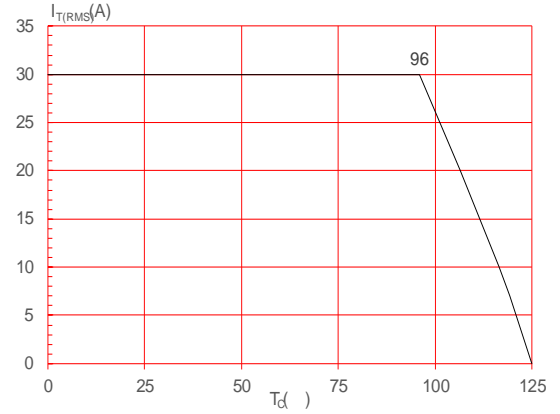


FIG.3: RMS on-state current versus ambient temperature (printed circuit board FR4,copper W K L F N Q H V V P I X O O F \ F O H

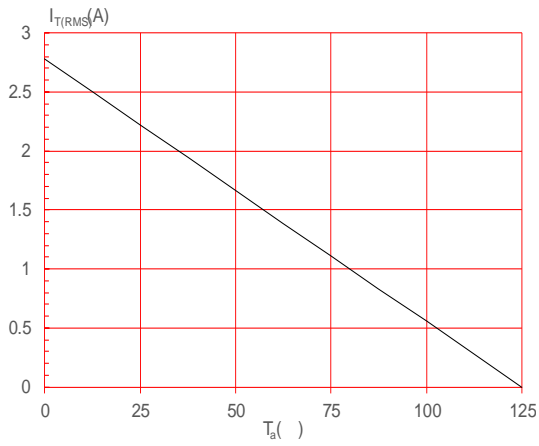


FIG.4: Surge peak on-state current versus number of cycles

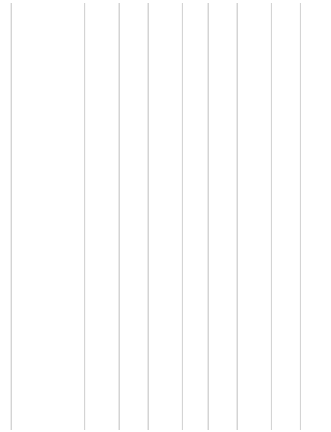
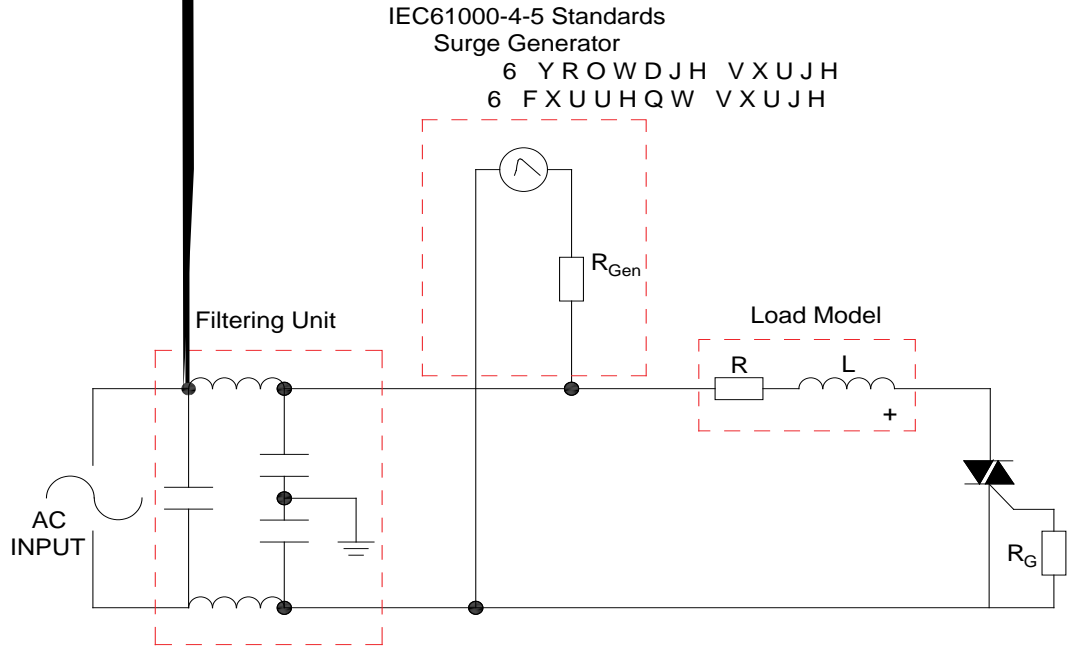


FIG.7: Relative variations of gate trigger current, holding current and latching current versus

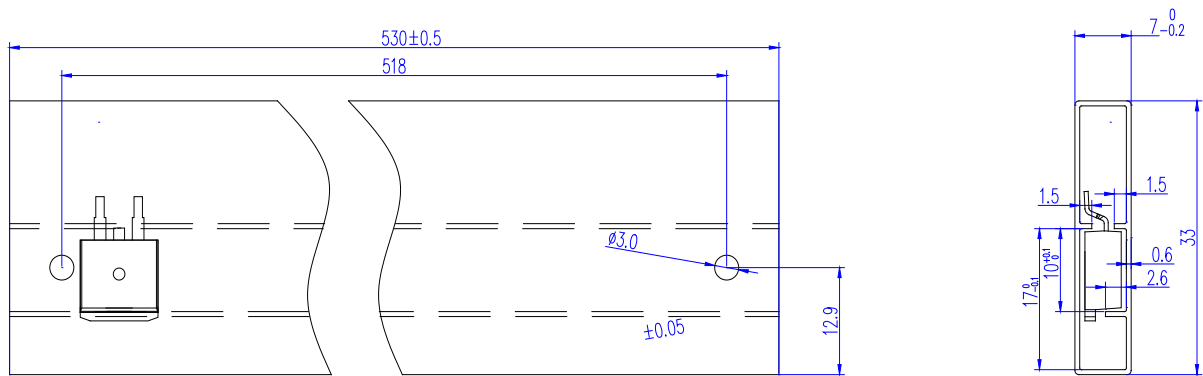
junction temperature 47821 1 re f Q q 56.81 573.56 198.6 158.25 re W n q 0.3301823 0 0 -0.3610339 47.0544473 7



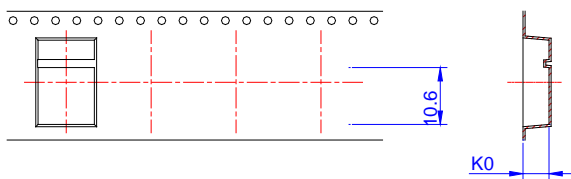
FIG.8: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



DELIVERY MODE



| PACKAGE | OUTLINE | TUBE (PCS) | INNER BOX (PCS) | PER CARTON |
|---------|---------|------------|-----------------|------------|
| TO-263 | TUBE | 50 | 1,000 | 5,000 |



| Ref. | Dimensions | | | | | |
|------|-------------|-------|-------|--------|-------|-------|
| | Millimeters | | | Inches | | |
| | Min. | Typ. | Max. | Min. | Typ. | Max. |
| W | 23.70 | 24.00 | 24.30 | 0.933 | 0.945 | 0.957 |
| E | 1.65 | 1.75 | 1.85 | 0.065 | 0.069 | 0.073 |
| F | 11.40 | 11.50 | 11.60 | 0.449 | 0.453 | 0.457 |
| D0 | - | 1.50 | 1.60 | - | 0.059 | 0.063 |
| D1 | - | 1.50 | 1.60 | - | 0.059 | 0.063 |
| P0 | 3.90 | 4.00 | 4.10 | 0.154 | 0.157 | 0.161 |
| P1 | 15.90 | 16.00 | 16.10 | 0.626 | 0.630 | 0.634 |
| P2 | 1.90 | 2.00 | 2.10 | 0.075 | 0.079 | 0.083 |
| A0 | 10.80 | 10.90 | 11.00 | 0.425 | 0.429 | 0.433 |
| B0 | 16.20 | 16.30 | 16.40 | 0.638 | 0.642 | 0.646 |
| K0 | 4.80 | 4.90 | 5.00 | 0.189 | 0.193 | 0.197 |
| t | 0.35 | 0.40 | 0.45 | 0.014 | 0.016 | 0.018 |

Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.



is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.

Copyright © 2025 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.