



DESCRIPTION:

The JST26Z-800B triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. By using an internal ceramic pad, JST26Z-800B provides a rated insulation voltage of 2500 VRMS, complying with UL standards (File ref: E252906). Package TO-3P is RoHS compliant

MAIN FEATURES

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T _{stg}	-40-150	°C
Operating junction temperature range	T _j	-40-125	°C

Peak pulse voltage ($T_j=25$; non-repetitive, of state; FIG.7)	V_{pp}	2	kV
---	----------	---	----

ELECTRICAL CHARACTERISTICS (unless otherwise specified)

Symbol	Test Condition	Quadrant	Value	Unit	
I_{GT}	$V_D=12V R_L=33$	- -	MAX.	50	mA
				70	
V_{GT}		ALL	MAX.	1	V
V_{GD}	$V_D=V_{DRM} T_j=125$ $R_L=3.3k$	ALL	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	- -	MAX.	80	mA
				120	
I_H	$I_T=500mA$		MAX.	80	mA
dV/dt	$V_D=540V$ Gate Open $T_j=125$		MIN.	1000	V/s
$(dV/dt)_c$	$(dI/dt)_c \approx 3.3A/ms, T_j=125$		MIN.	12	9 V
t_{on}	$I_G=80mA I_A=400mA I_R=40mA$ $T_j=25$		TYP.	3	s
t_{off}				50	

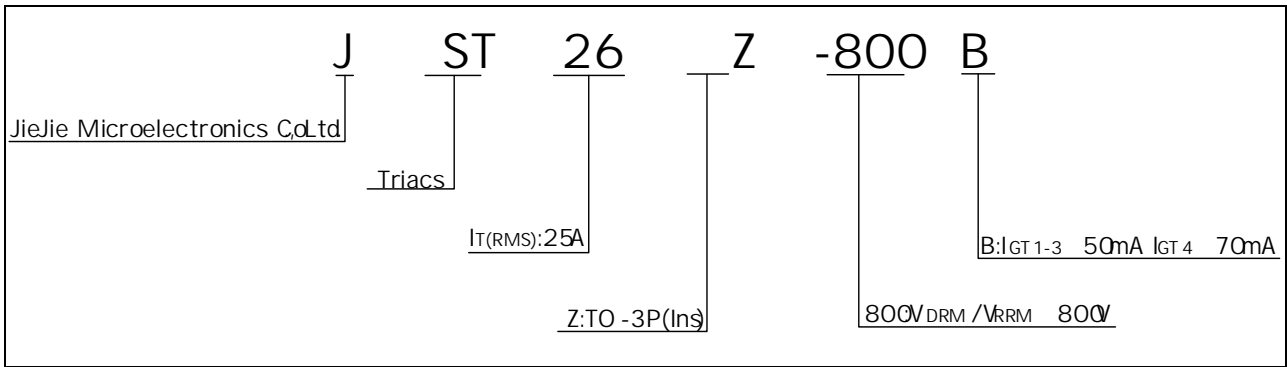
STATIC CHARACTERISTICS

Symbol	Parameter	Value(MAX.)	Unit	
V_{TM}	$I_{TM}=35A \phi=380s$ $T_j=25$	1.5	V	
V_{TO}	Threshold voltage $T_j=125$	0.75	V	
R_D	Dynamic resistance $T_j=125$	18	P	
I_{DRM}	$V_D=V_{DRM} V_R=V_{RRM}$	$T_j=25$	5	A
I_{RRM}		$T_j=125$	2	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(jc)}$	junction to case (AC)	1.1	/W

ORDERING INFORMATION



MARKING

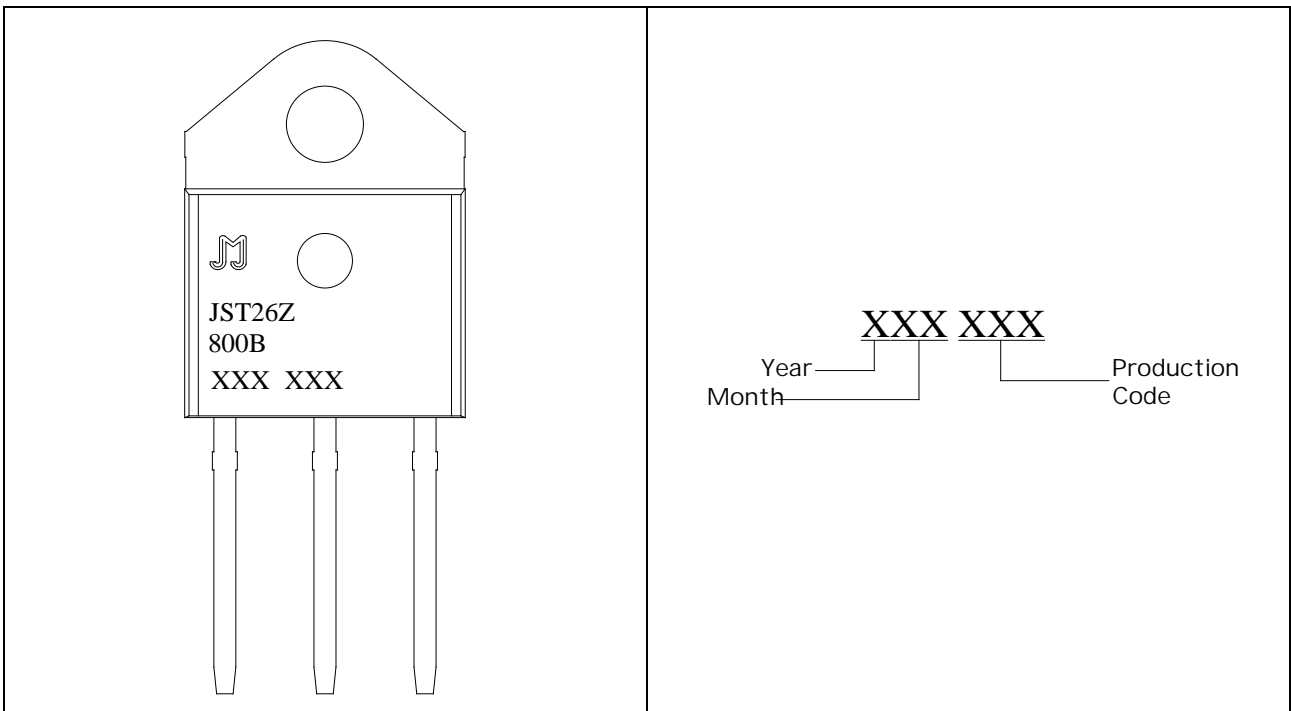


FIG.1: Maximum power dissipation versus RMS on-state current

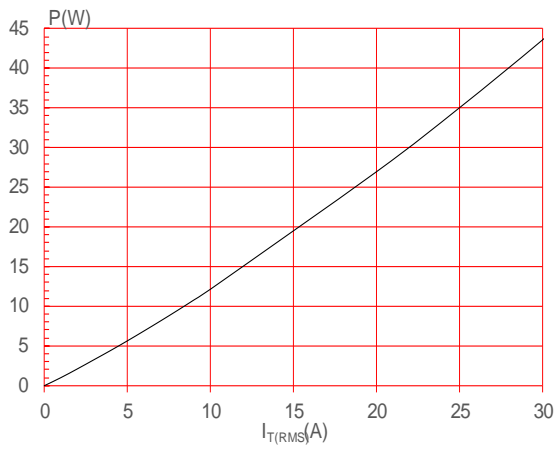


FIG.3: Surge peak onstate current versus number of cycles

FIG.2: RMS on-state current versus case temperature

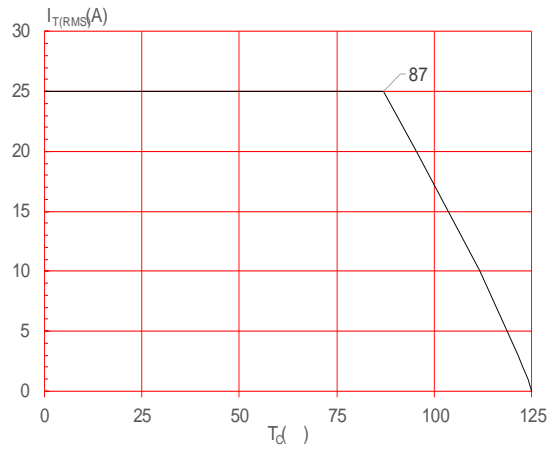
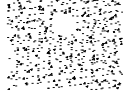


FIG.4: On-state characteristics

ORDERING INFORMATION

Order 5.0.011 To

PACKAGE MECHANICAL DATA



Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd. assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement. Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously published.