



JST136Q-800D 4A TRAC

Rev. A.1.0

DESCRIPTION

The JST136Q800D is a surface-mount general purpose
 A 4-pin 136-pin package with 800 pins in
 pin 1 is on sub package, inductor motor
 status indicator for phase control operation in light
 diode motor speed control less From T2 to minasto
 external heat sink. Page TO-126 is RoHS compliant.

MECHANICAL

ABBREVIATIONS

Parameter	Symbol	Value	Unit
Storage junction temperature	T_{sg}	-40 150	
Operating junction temperature	T_j	-40 125	
Reverse peak off-state voltage ($T_j=25^\circ\text{C}$)	V_{RM}	800	V
Reverse peak on-state voltage ($T_j=25^\circ\text{C}$)	V_{RRM}	800	V
RMS on-state current ($T_c \leq 83^\circ\text{C}$)	$I_{T(RMS)}$	4	A
Non repetitive surge peak on-state current (full cycle, $t_p=20\text{ms}$, $T_j=25^\circ\text{C}$)	I_{TSM}	35	A
Non repetitive surge peak on-state current (full cycle, $t_p=16.6\text{ms}$, $T_j=25^\circ\text{C}$)		38.5	
I^2t value for fusing ($t_p=10\text{ms}$, $T_j=25^\circ\text{C}$)	I^2t	6.1	A^2s

Gate threshold voltage -
 ($I_G=2 \times I_{GT}$, $f=100\text{Hz}$, $T_j=125^\circ\text{C}$)

ELECTRICAL CHARACTERISTICS

(Tj=25 unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
IGT	VD=12V RL=33Ω	- -	MAX.	5	mA
				10	
VGT		ALL	MAX.	1	V
VGD	VD=VBM Tj=125 RL=3.3kΩ	ALL	MIN.	0.2	V
IL	IG=1.2IGT	-	MAX.	15	mA
		-		25	
IH	IT=10mA		MAX.	15	(Refer to Tj 0 Tc 0 T)
dV/dt	VD=54V Gate n Tj=110		MIN.	100	V/μs
(dV/dt)c	(dIDt)≤1.8 A/ns Tj=110		MIN.	2.5	V/μ

FG1 : Minum power di sip on v s R M
on - sear rent

FG2: R M on - sear rent v s e
tempr a ure

PARAMETERS

Order code	Voltage V_{DD}/V_{SS} (V)	I _G (mA)		Package	Base qty. (pcs)	Delivery mode
		-	-			
JST136Q-800D	800	5	10	TO-126	500	Bulk Pack

Document Revision History

No.	Reason	Days
Apr.14, 2023	A 1.0	Launched

ST13600D

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