

Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.8)	V_{pp}	3.5	kV
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($T_j=25$ unless otherwise specified)

Symbol Test Condition Quadrant

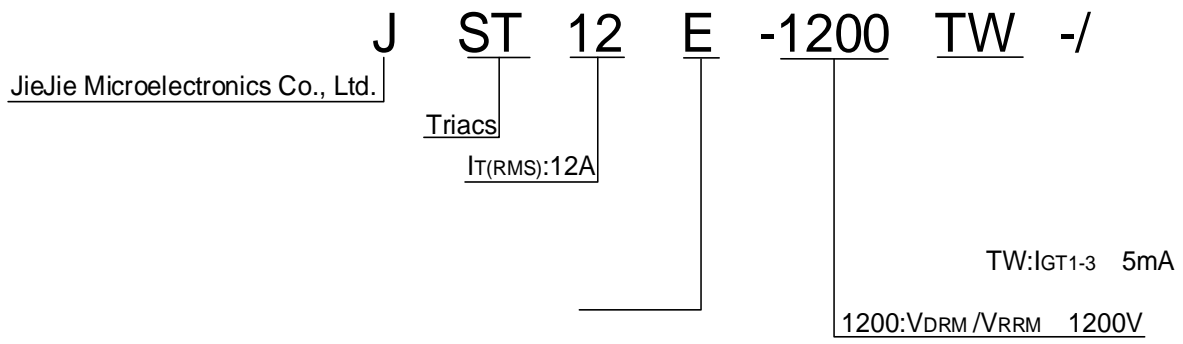


FIG.1: Maximum power dissipation versus RMS on-state current

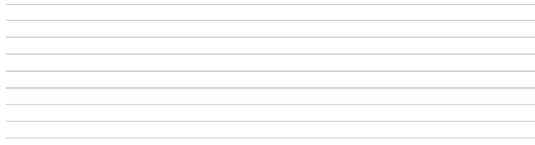


FIG.2: RMS on-state current versus case temperature

FIG.7: Relative variations of gate trigger atins(el)7.1 (at)6.3 (i)-4.3 (v)12.4 (e v)12.3 (ar)4.5 (i)-4.3 (at)6.3 (i)-4.3 (o)11

FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards

