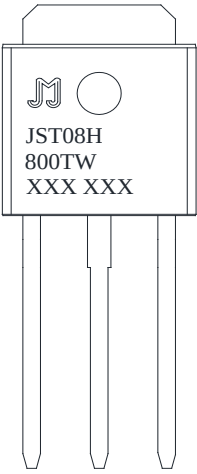


Peak gate power	P_{GM}	10	W
Peak pulse voltage ($T_j=25$; non-repetitive,off-state;FIG.7)	V_{pp}	1.5	kV

ORDERING INFORMATION

<u>J</u>	<u>ST</u>	<u>08</u>	<u>H</u>	<u>-800</u>	<u>TW</u>
JieJie Microelectronics Co., Ltd.	Triacs				
	<u>IT(RMS):8A</u>				
		<u>H:TO-251</u>			
				<u>800:VDRM /VRRM</u>	<u>TW:IGT1-3 5mA</u>
				800V	

MARKING

 The diagram shows a TO-251 package with a marking area containing the following text: JM JST08H 800TW XXX XXX	XXX XXX Year Month Production Code
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JST08H-800TW

FIG.7 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



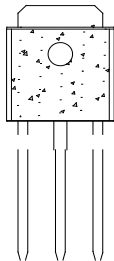
ORDERING INFORMATION

Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		- -			
JST08H-800TW	800	5	TO-251	80	Tube

Document Revision History

Date	Revision	Changes
Apr.12, 2023	A.1.0	Last updated
Oct.16, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

PACKAGE MECHANICAL DATA



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