

Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.7)	V_{pp}	4.5	kV
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($T_j=25$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V R_L=33$	- -	MAX.	35	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	$V_D=V_{DRM} T_j=150$ $R_L=3.3k$	- -	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	-	MAX.	30	mA
				60	
I_H	$I_T=500mA$		MAX.	30	mA
dV/dt	$V_D=540V$ Gate Open $T_j=150$		MIN.	500	V s
(dI/dt) _c	$j=150$		MIN.	5	A/ms
t_{on}	$I_G=40mA I_A=200mA I_R=20mA$ $T_j=25$		TYP.	3	s
t_{off}				60	

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=17A t_p=380 s$	$T_j=25$	1.4	V
V_{TO}	Threshold voltage	$T_j=150$	0.75	V
R_D	Dynamic resistance	$T_j=150$	37	
I_{DRM}	$V_D=V_{DRM} V_R=V_{RRM}$	T_j		

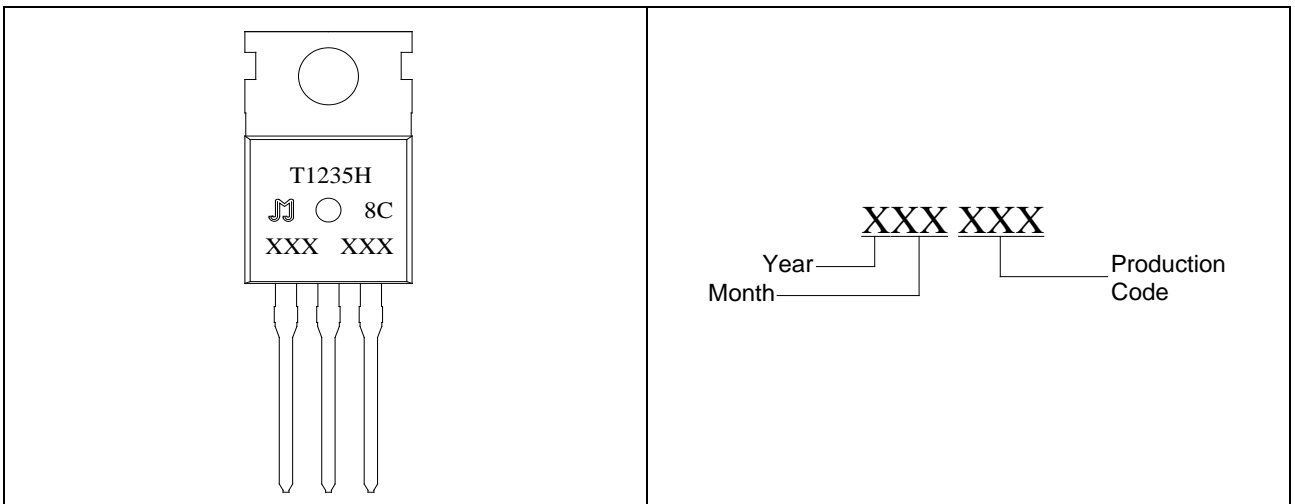
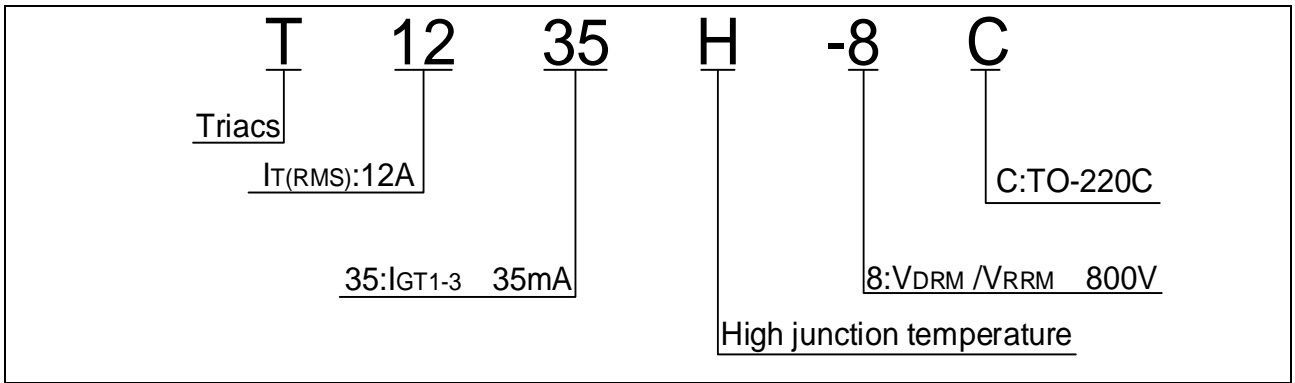


FIG.1: Maximum power dissipation versus RMS on-state current

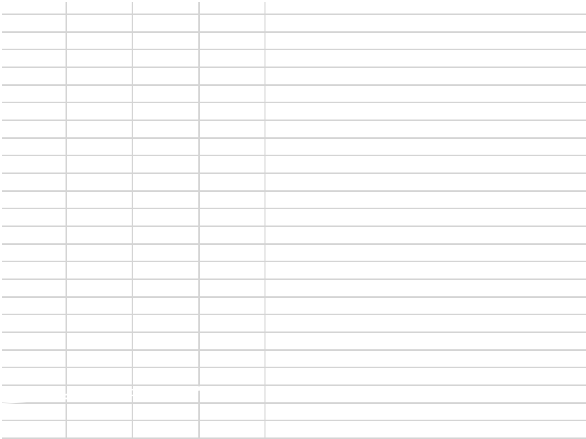


FIG.2: RMS on-state current versus case temperature

FIG.7 Test circuit for inductive and resistive loads to IEC

Order code	Voltage V_{DRM}/V_{RRM} (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
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