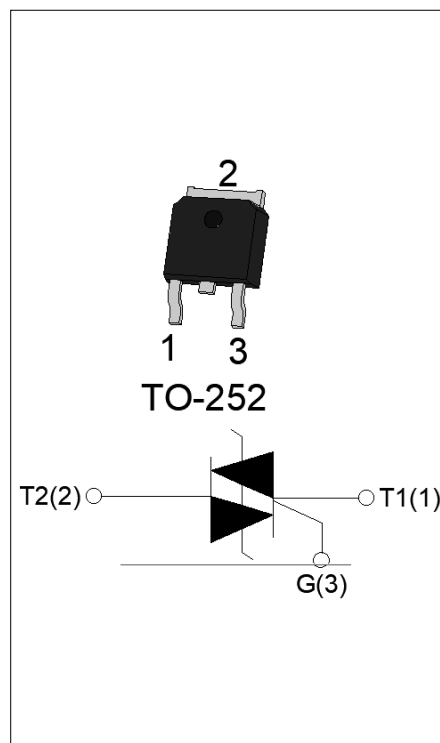


ACJT810-8K 8A TRIAC

Rev.A.1.2

DESCRIPTION:

The ACJT810-8K triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. The ACJT810-8K embeds a TVS structure to absorb the inductive turn-off energy such as those described in the IEC 61000-4-5 standards. Package TO-252 is RoHS compliant.


MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	8	A
V_{DRM}/V_{RRM}	800	V
$I_{GT} / /$	10/10/10	mA

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Value	Unit
Storage junction temperature range	T_{stg}	-40-150	
Operating junction temperature range	T_j	-40-125	
Repetitive peak off-state voltage ($T_j=25^\circ C$)	V_{DRM}	800	V
Repetitive peak reverse voltage ($T_j=25^\circ C$)	V_{RRM}	800	V
RMS on-state current ($T_c \leq 107^\circ C$)	$I_{T(RMS)}$	8	A
Non repetitive surge peak on-state current (full cycle, $t_p=20ms$, $T_j=25^\circ C$)	I_{TSM}	80	A
Non repetitive surge peak on-state current (full cycle, $t_p=16.6ms$, $T_j=25^\circ C$)		88	
I^2t value for fusing ($t_p=10ms$, $T_j=25^\circ C$)	I^2t	32	A^2s
Critical rate of rise of on-state current ($I_G=2 \times I_{GT}$, $f=100Hz$, $T_j=125^\circ C$)	di/dt	100	$A/\mu s$
Peak gate current ($t_p=20\mu s$, $T_j=125^\circ C$)	I_{GM}	4	A
Average gate power dissipation ($T_j=125^\circ C$)	$P_{G(AV)}$	0.5	W
Peak gate power	P_{GM}	10	W

Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.8)	V_{pp}	2	kV
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ELECTRICAL CHARACTERISTICS (at $T_j=25$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12V$ $R_L=33\Omega$	- -	MAX.	10	mA
V_{GT}		- -	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125$ $R_L=3.3k\Omega$	- -	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	-	MAX.	25	mA
				30	
I_H	$I_T=100mA$		MAX.	15	mA
dV/dt	$V_D=540V$ Gate Open $T_j=125$		MIN.	500	V/ μs
$(dI/dt)_C$	$(dV/dt)_C=10V/\mu s$, $T_j=125$		MIN.	3	A/ms
t_{on}	$I_G=20mA$ $I_A=200mA$ $I_R=20mA$ $T_j=25$		TYP.	2.5	μs
t_{off}				25	
V_{CL}	$I_{CL}=0.1mA$ $t_p=1ms$		MIN.	850	V

STATIC CHARACTERISTICS

Symbol	Parameter	Value(MAX.)	Unit
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 V_{TM} I

ORDERING INFORMATION

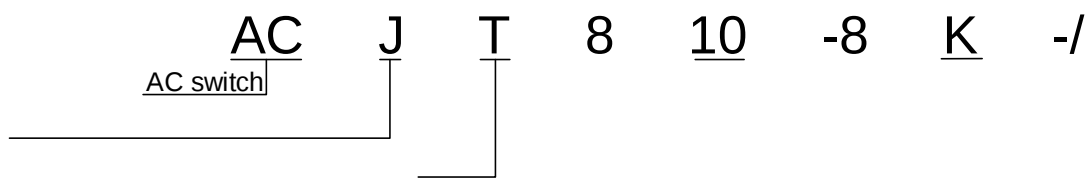


FIG.1: Maximum power dissipation versus RMS on-state current

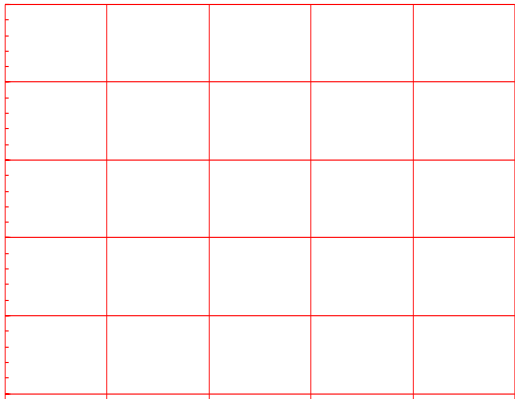


FIG.2: RMS on-state current versus case temperature

FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

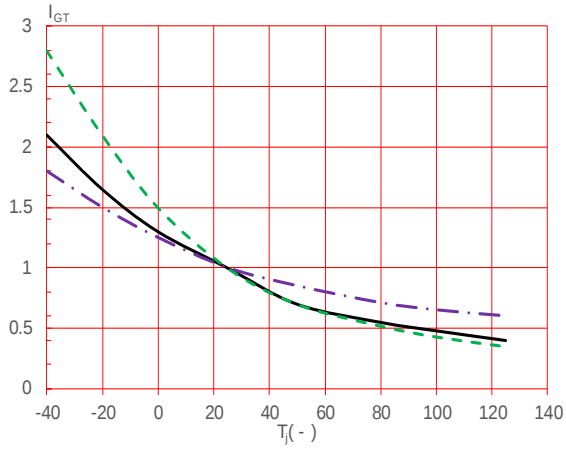


FIG.8 Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



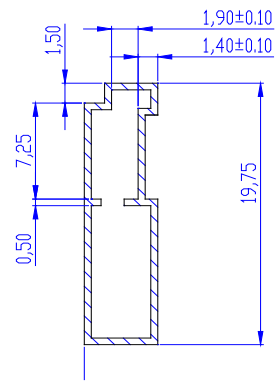
ORDERING INFORMATION

Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)	Package	Base qty.
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Technical drawing of a rectangular component. The drawing includes a top view and a side view. The top view shows a rectangle with dimensions E (total width), B2 (inner width), J (flange thickness), H (total height), B (base thickness), and G (inner width). A central circular feature is indicated by a red dashed line. The side view shows the component's profile with a curved bottom edge and a dimension C2 for the base thickness.

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.15	0		0.006
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1						
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
G1	2.18		2.38	0.086		0.094
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065

DELIVERY MODE



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