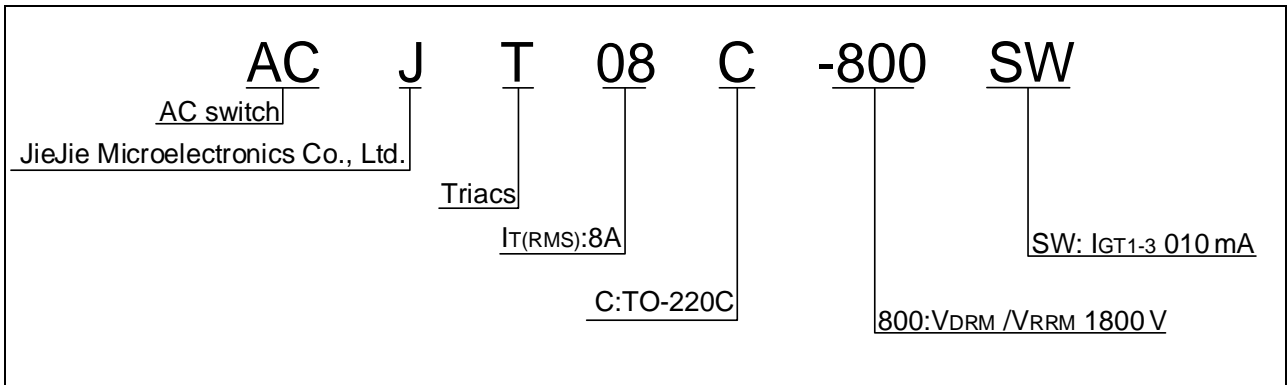


Peak pulse voltage ($T_j=25$; non-repetitive, off-state; FIG.7)	V_{pp}	2.25	kV
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ORDERING INFORMATION



MARKING

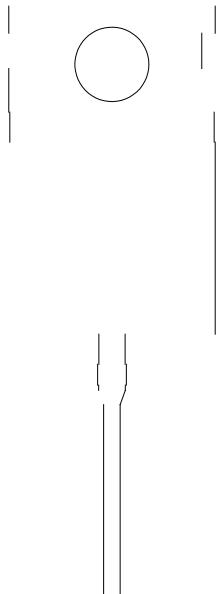


FIG.1: Maximum power dissipation versus RMS on-state current

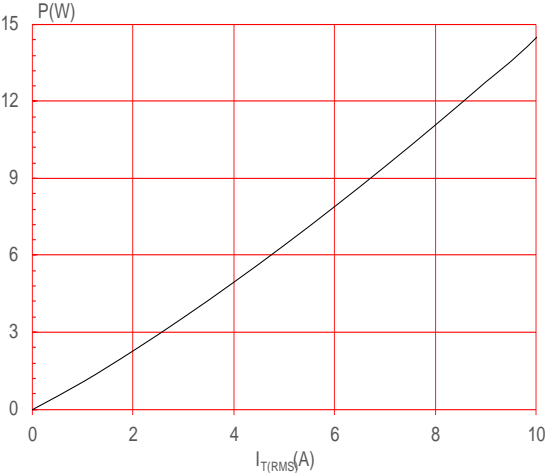


FIG.2: RMS on-state current versus case temperature

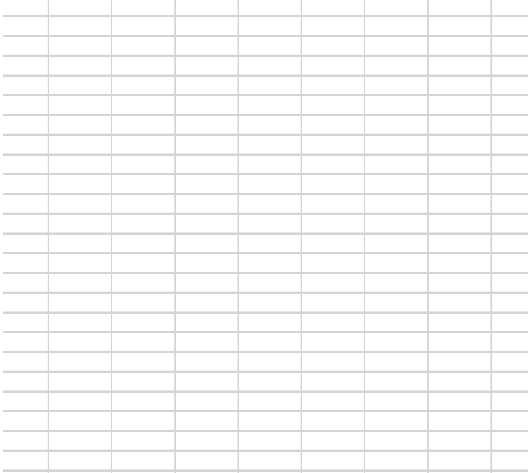
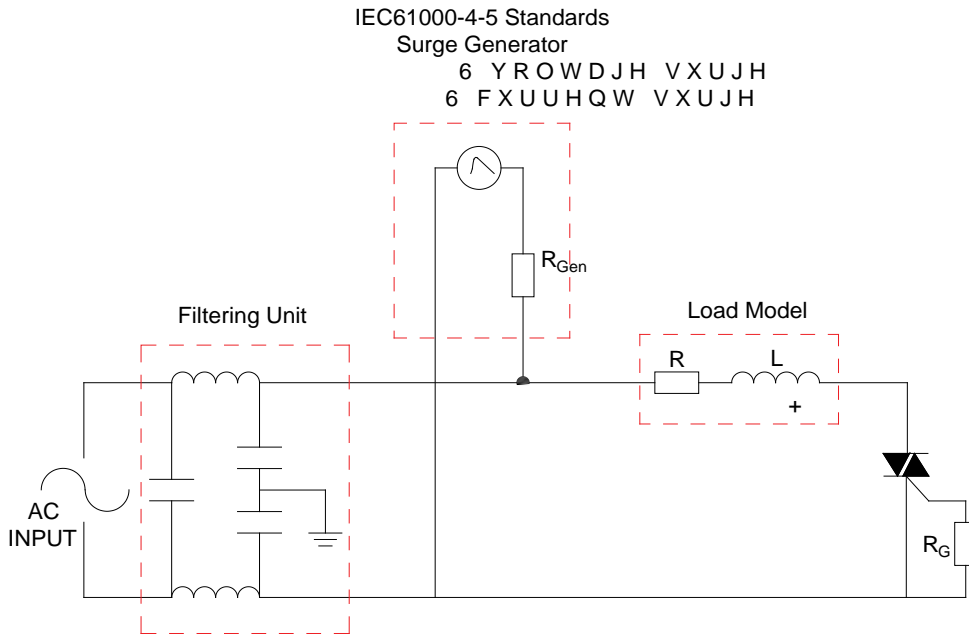


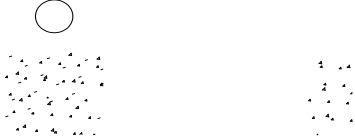
FIG.7 ÖTest circuit for inductive and resistive loads to IEC-61000-4-5 standards



ORDERING INFORMATION


Order code	Voltage V_{DRM}/V_{RRM}
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