



P0150SD-A TSS

Rev.1.1

DESCRIPTION:

P0150SD-A is a type of semiconductor component. It is designed to protect baseband equipment from damaging overvoltage transients, such as modems, telephones, line cards, answering machines, FAX machines, T1/E1, xDSL and more.

FEATURES:

- Low profile package.
- Low on-state voltage.
- Excellent capability of absorbing transient surge.
- Quick response to surge voltage (ns Level).
- Eliminates overvoltage caused by fast rising transients.
- UL 497B item recognized. (File No.:E480698).
- IEC61000-4-2 (ESD) ±30kV (air), ±30kV (contact).
- Moisture sensitivity level: Level 1.
- Non degenerative.

ABSOLUTE MAXIMUM RATINGS(T<sub>A</sub>=25 , RH=45%-75%, unless otherwise noted)

Parameter	Symbol	Value	Unit
Storage temperature range	T <sub>STG</sub>	-60 to +150	
Operating junction temperature range	T <sub>J</sub>		

MARKING



J12D: Device Marking Code  
2009: the ninth week, 2020

ELECTRICAL CHARACTERISTICS( $T_A=25$  , continued)

Part Number	$I_{DRM}@V_{DRM}$		$V_s @I_s$		$V_T@I_T$		$I_H$	$C_o$	Marking
	$\mu A$	V	V	mA	V	A	mA	pF	
	max		max	max	max	max	typ	max	
P0150SD-A	1	12	16	800	4	2.2	10	150	J12D

$V_s$  is measured at 100kV/s

Off-state capacitance is measured in  $V_{DC}=2V, V_{RMS}=1V, f=1MHz$

SURGE RATINGS

Series	$I_{PP}(A) \text{ min}$			
		2/10 $\mu s$	8/20 $\mu s$	10/360 $\mu s$


**SOLDERING PARAMETERS**

Reflow Condition		Pb-Free assembly (see FIG.2)
Pre Heat	-Temperature Min ( $T_{s(min)}$ )	+150
	-Temperature Max( $T_{s(max)}$ )	+200
	-Time (Min to Max) (ts)	60-180 secs.
Average ramp up rate (Liquidus Temp ( $T_L$ )to peak)		3 /sec. Max

PACKAGE MECHANICAL DATA

Ref.	Dimensions	
	Millimeters	Inches

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