



## JP15SB TSS

Rev.3.2

### DESCRIPTION:

JP15SB thyristors are a type of semiconductor component. They are designed to protect baseband equipment from damaging overvoltage transients. such as modems, telephones, line cards, answering machines, FAX machines, T1/E1, xDSL and more.

### FEATURES:

- ✧ Lower capacitance.
- ✧ Excellent capability of absorbing transient surge.
- ✧ Quick response to surge voltage (ns Level).
- ✧ Eliminates overvoltage caused by fast rising transients.
- ✧ Moisture sensitivity level: Level 1.
- ✧ IEC61000-4-2 (ESD) ±30kV (air), ±30kV (contact).
- ✧ Non degenerative.

### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub>=25°C, RH=45%-75%, unless otherwise noted)

Parameter	Symbol	Value	Unit
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**SOLDERING PARAMETERS**

Reflow Condition		Pb-Free assembly (see FIG.2)
Pre Heat	-Temperature Min ( $T_{s(min)}$ )	+150
	-Temperature Max( $T_{s(max)}$ )	+200
	-Time (Min to Max) (ts)	60-180 secs.
Average ramp up rate (Liquidus Temp ( $T_L$ )to peak)		3 /sec. Max
$T_{s(max)}$ to $T_L$ - Ramp-up Rate		3 /sec. Max
Reflow	-Temperature( $T_L$ ) (Liquidus)	+217
	-Temperature( $t_L$ )	60-150 secs.



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