

FEATURES

- 6000 Watts peak pulse power dissipation($t_P=8/20\mu s$)
- Low leakage current
- Low clamping voltage
- Solid-state silicon-avalanche technology
- RoHS compliant

MAIN APPLICATIONS

- Power lines
- Personal digital assistants (PDA's)
- Microprocessors based equipment
- Notebooks, desktops, and servers
- Cell phone handsets and accessories
- Portable electronics
- Peripherals

PROTECTION SOLUTIONS WE MEET

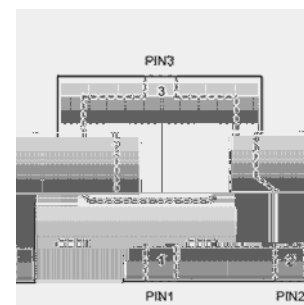
- IEC61000-4-2 (ESD) $\pm 30kV$ (air), $\pm 30kV$ (contact)
- IEC61000-4-4 (EFT) 40A (5/50ns)
- IEC61000-4-5 (Lightning) 120A (8/20 μs)

MECHANICAL CHARACTERISTICS

- DFN2x2-3L package
- Molding compound flammability rating: UL 94V-0
- Quantity per reel: 3,000pcs
- Lead finish: lead free
- Marking code: T24



Circuit Diagram



Pin Configuration

ABSOLUTE MAXIMUM RATINGS (T_A=25 °C, RH=45%-75%, unless otherwise noted)

Symbol	Parameter	Value	Unit
P _{PP}	Peak pulse power dissipation on 8/20μs waveform	6000	W
V _{ESD}	ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	+/- 30 +/- 30	kV
T _L	Lead soldering temperature	260 (10 sec.)	
T _J	Operating junction temperature range	-55 to +125	
T _{STG}	Storage temperature range	-55 to +150	

ELECTRICAL CHARACTERISTICS (T_A=25 °C)

Symbol	Parameter	Conditions	Value	Unit
V _{RWM}	Reverse working voltage		24	V
V _{BR}	Reverse breakdown voltage	I _T =1mA	26, 27, 30	V
I _R	Reverse leakage current	V _{RWM} =24V	1	μA
V _C	Clamping voltage	I _{PP} =50A, t _p =8/20μs	45, 50	V
		I _{PP} =120A, t _p =8/20μs	52, 60	
C _J	Junction capacitance	V _{RWM} =0V, f=1MHz	750	pF

RATINGS AND CHARACTERISTIC CURVES (T_A=25 °C, unless otherwise noted)

Fig. 1

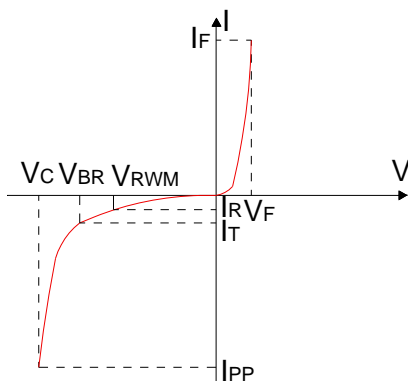
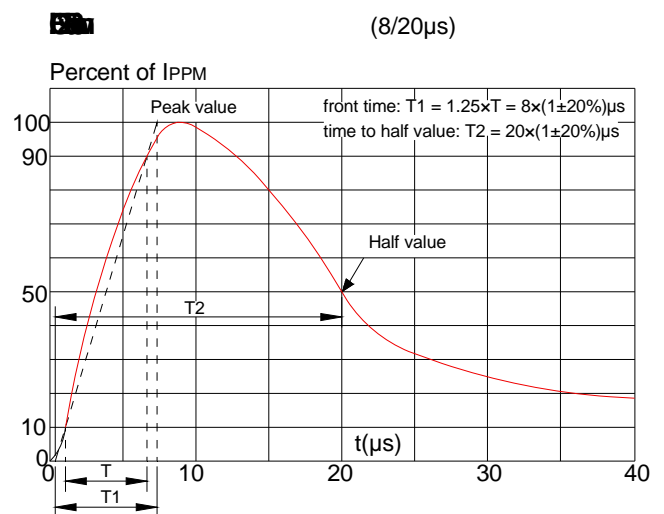
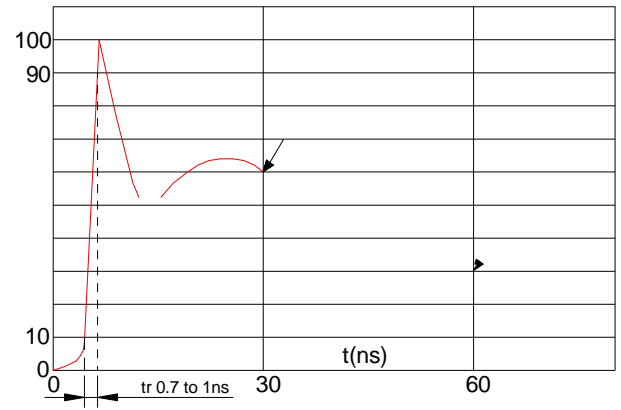
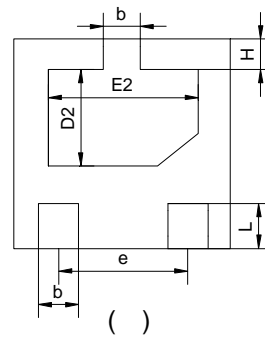
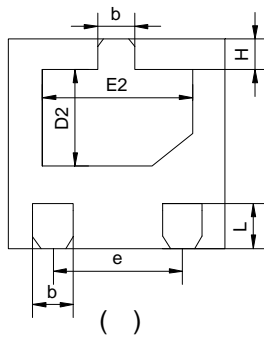
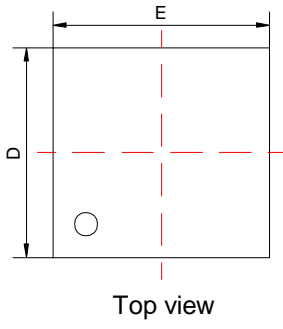


Fig. 2





PACKAGE MECHANICAL DATA



Bottom view i233(w)JTJ 8059937 re f 274.5 633.02 NT q 1

MARKING CODE

Part Number	Marking Code
JEU24N3	<div style="border: 1px solid black; padding: 10px; display: inline-block;"> <p style="text-align: center;">T24 ● 003</p> </div>

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