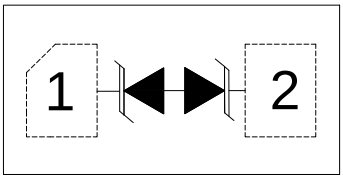
JEB4V5DFP Bi-directional TVS Diode for ESD Protection Rev.1.1

FEATURES

Solid-state silicon technology
Low clamping voltage and low leakage current
Working voltage: 4.5V
Ultra-low capacitance
RoHS compliant

DFN1006-2L (Bottom view)



Pin Configuration (Top view)

MAIN APPLICATIONS

Cellular handsets
Tablets
Laptops
Network communication devices
Other portable devices

PROTECTION SOLUTION TO MEET

IEC61000-4-2 (ESD) ±30kV (air), ±30kV (contact)
IEC61000-4-4 (EFT) 40A (5/50ns)
IEC61000-4-5 (Lightning) 27A (8/20μs)

MECHANICAL CHARACTERISTICS

DFN1006-2L package
Molding compound flammability rating: UL 94V-0
Marking code: 4G
Quantity per reel: 10,000pcs
Lead finish: lead free

ABSOLUTE MAXIMUM RATINGS(T_A=25 , RH=45%-75%, unless otherwise noted)

Parameter	Symbol	Value	Unit
Peak pulse power dissipation at 8/20μs waveform	P_{PP}	300	W
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V_{ESD}	+/- 30 +/- 30	kV
Lead soldering temperature	T_L	260 (10 sec.)	
Operating junction temperature range	T_J	-55 to +125	
Storage temperature range	T_{STG}	-55 to +150	

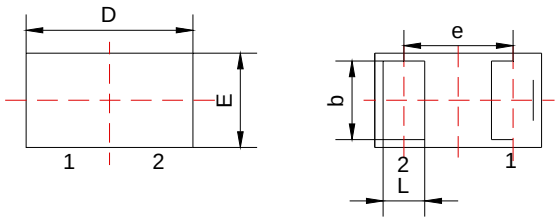
ELECTRICAL CHARACTERISTICS(T_A=25)

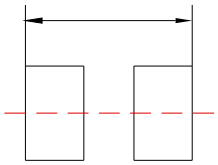
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Reverse working voltage	V_{RWM}				4.5	V
Reverse breakdown voltage	V_{BR}	I_T=1mA	4.6			V
Reverse leakage current	I_R	V_{RWM}=4.5V			1.0	μA
Peak pulse current	I_{PP}	t_P=8/20μs			27	A
Clamping voltage	V_C	I_{PP}=16A, t_P=100ns		7		V
Dynamic resistance	R_{DYN}			0.09		Ω
Clamping voltage	V_C	V_{ESD}=8kV		9		V
Clamping voltage	V_C	I_{PP}=1A, t_P=8/20μs		5	6	V
		I_{PP}=20A, t_P=8/20μs		8	9.5	V
		I_{PP}=27A, t_P=8/20μs		10	11	V
Junction capacitance	C_J	V_{RWM}=0V, f=1MHz		80	95	pF u *f*

SOLDERING PARAMETERS

Reflow Condition

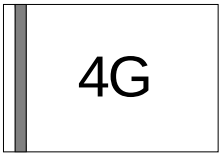
PACKAGE MECHANICAL DATA





TAPE AND REEL INFORMATION-DFN1006-2L

MARKING CODE

Part Number	Marking Code
JEB4V5DFP	

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