Description

JMT Dual P-channel Enhancement Mode Power MosFET

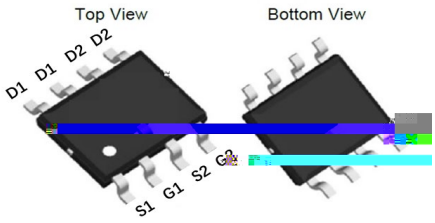
Features

Ω @

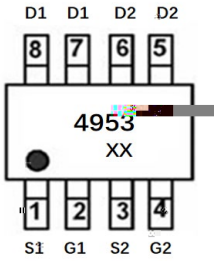
Ω @

Applications

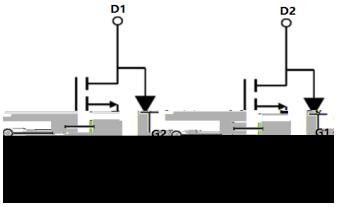
100% UIS TESTED!
100% ΔVds TESTED!



SOP-8



Marking and Pin Assignment



Schematic Diagram

Package Marking and Ordering Information

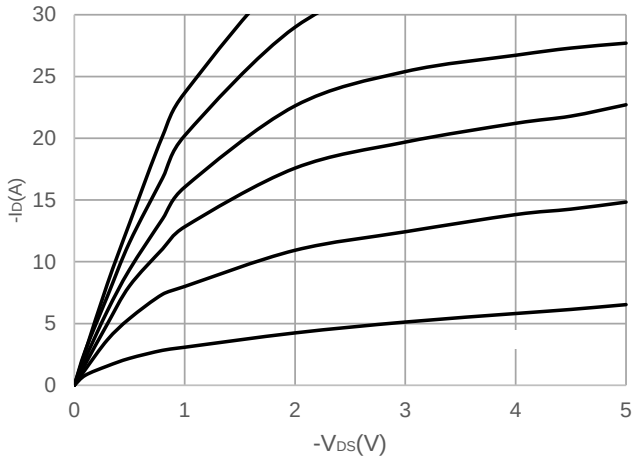
Device Marking	Device	Outline	Package	Reel Size		

Absolute Maximum Ratings

I_D	Parameter		Value	A
				℃

Typical Performance Characteristics

Figure 1: Output Characteristics



Test Circuit

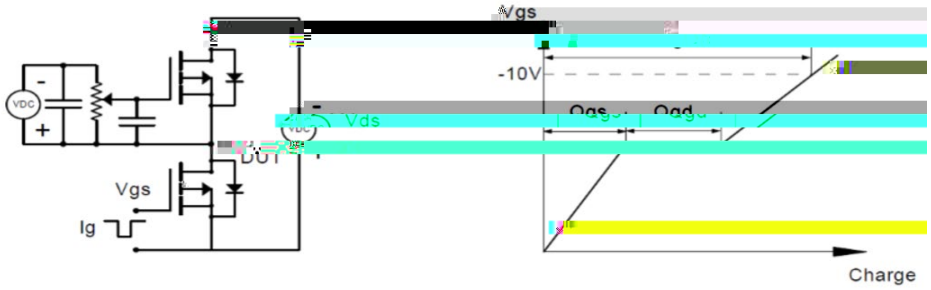


Figure 1: Gate Charge Test Circuit & Waveform

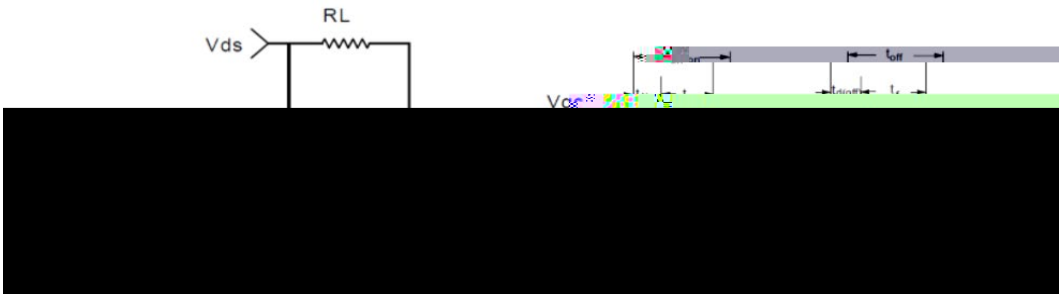


Figure 2: Resistive Switching Test Circuit & Waveform

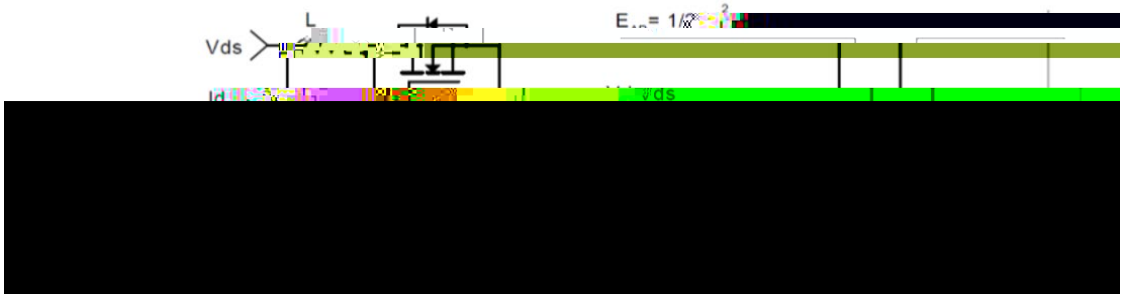


Figure 3: Unclamped Inductive Switching Test Circuit& Waveform

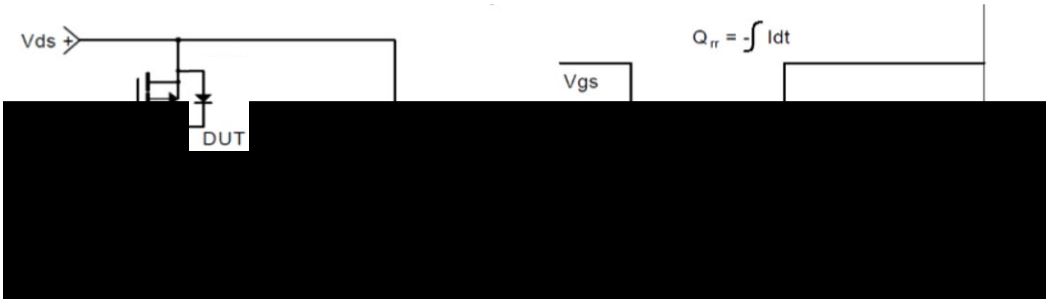
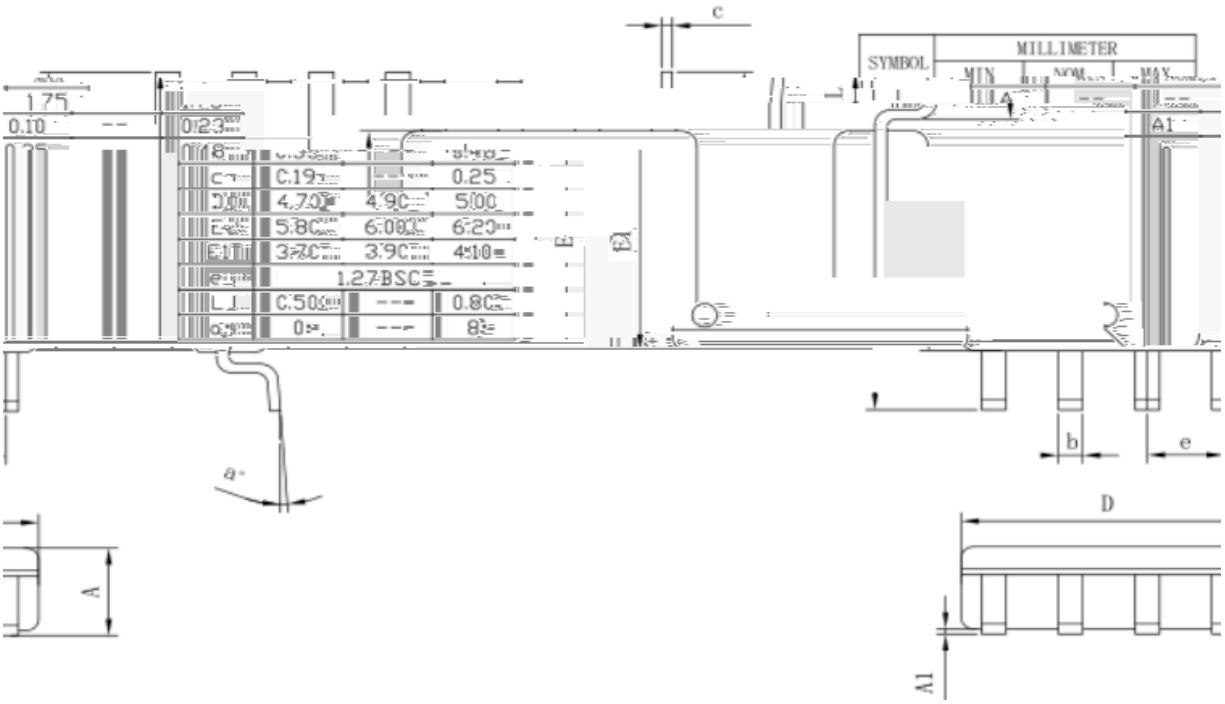


Figure 4: Diode Recovery Test Circuit & Waveform

Package Mechanical Data(SOP-8)