



# JMSH1552PK

## Features

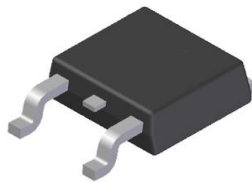
- Excellent  $R_{DS(ON)}$  and Low Gate Charge
- 100% UIS Tested
- 100% Vds Tested
- Halogen-free; RoHS-compliant

## Applications

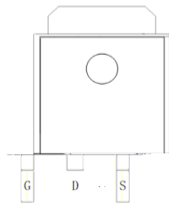
- Load Switch
- PWM Application
- Power Management

## Product Summary

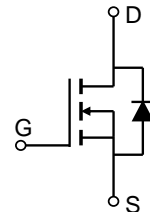
Parameters	Value	Unit
$V_{DSS}$	150	V
$V_{GS(th\_Typ)}$	3.3	V
$I_D(@V_{GS}=10V)$	17	A
$R_{DS(ON)\_Typ}(@V_{GS}=10V)$	48	mΩ



TO-252-3L



Pin Assignment



Schematic Diagram

## Ordering Information

Device	Marking	MSL	Form	Package	Reel(pcs)	Per Carton (pcs)
JMSH1552PK-13	SH1552P	3	Tape&Reel	TO-252-3L	2500	25000

## Absolute Maximum Ratings (@ $T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Unit
$V_{DS}$	Drain-to-Source Voltage	150	V
$V_{GS}$	Gate-to-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current	$T_C = 25^\circ\text{C}$	17
		$T_C = 100^\circ\text{C}$	11
$I_{DM}$	Pulsed Drain Current <sup>(1)</sup>	Refer to Fig.4	A
$E_{AS}$	Single Pulsed Avalanche Energy <sup>(2)</sup>	60	mJ
$P_D$	Power Dissipation	$T_C = 25^\circ\text{C}$	40
		$T_C = 100^\circ\text{C}$	16
$T_J, T_{STG}$	Junction & Storage Temperature Range	-55 to 150	$^\circ\text{C}$

## Thermal Characteristics

Symbol	Parameter	Max	Unit
R	Thermal Resistance, Junction to Ambient <sup>(3)</sup>	42	$^\circ\text{C}/\text{W}$
R	Thermal Resistance, Junction to Case	3.1	



## Electrical Characteristics

[Redacted]

[Redacted]

[Redacted]

[Redacted]

[Redacted]

# Typical Performance Characteristics





### Test Circuit



Figure 1: Gate Charge Test Circuit & Waveform

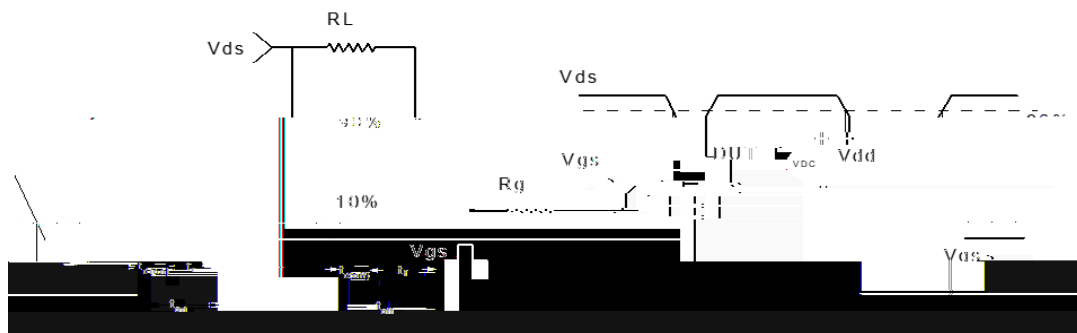


Figure 2: Resistive Switching Test Circuit & Waveform

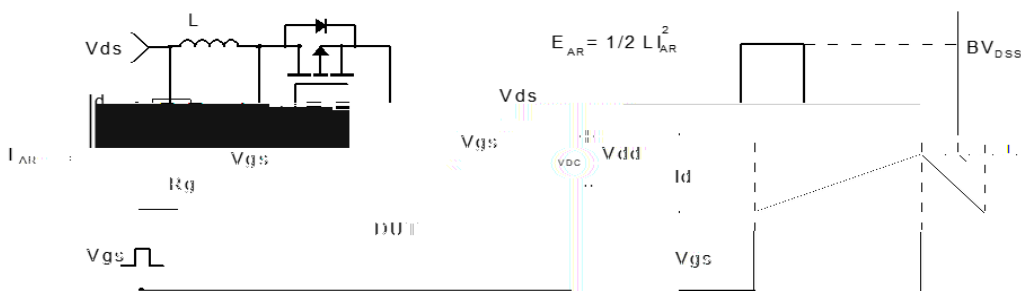


Figure 3: Unclamped Inductive Switching Test Circuit & Waveform

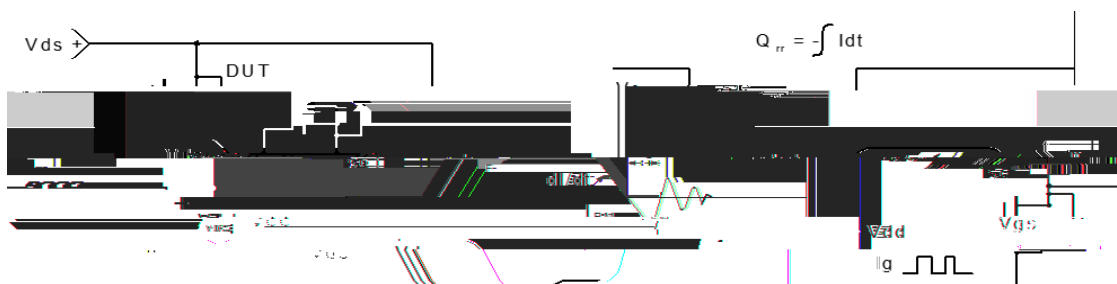
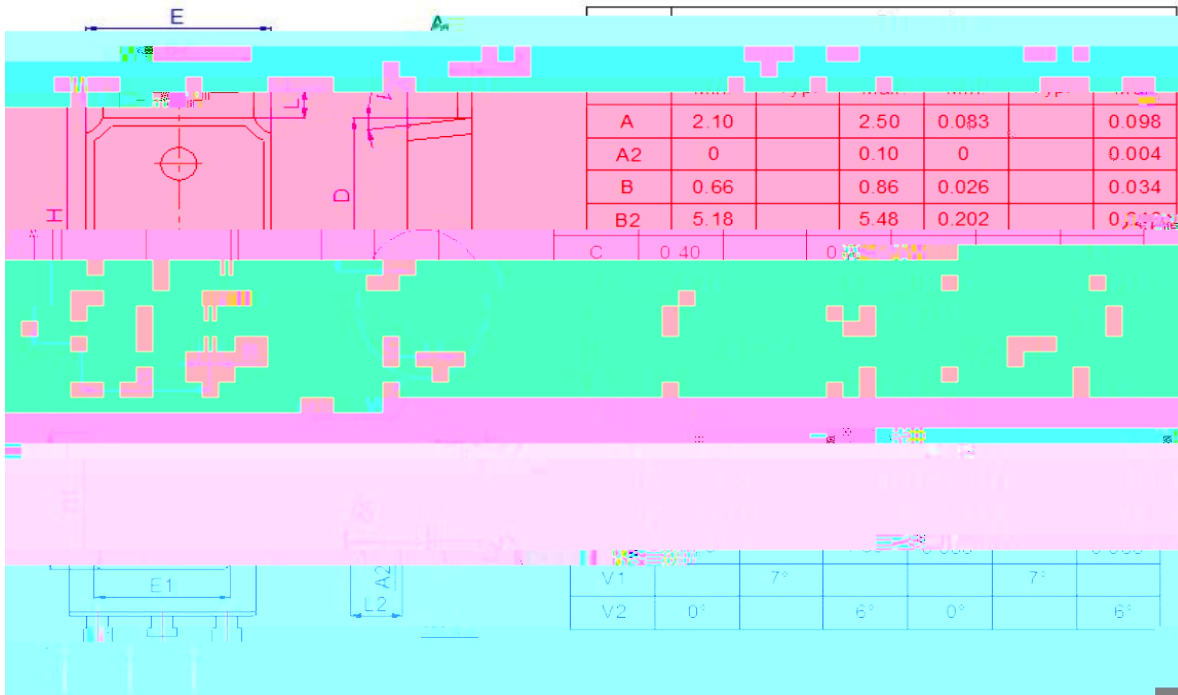


Figure 4: Diode Recovery Test Circuit & Waveform





### Package Mechanical Data(TO-252-3L)



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